

**PROJECT- Senior Management Positions/Management Positions  
(ProjectCode-C-01) Position → One Position for each Work Area Code**

<b>WORK AREA CODE</b>	<b>Work Area</b>	<b>SKILL SET</b>	<b>Experience</b>
C-0101	Post of Registrar	<p>The incumbent should be a highly competent Management Professional, possessing exceptional inter-personal and communication skills, for C-DOT's top management post of the REGISTRAR, who along with his full-time management responsibilities, also works with Project Board, Steering Committee and the Governing Council of the Centre.</p> <p><b>PROFILE OF THE REGISTRAR</b></p> <ul style="list-style-type: none"> <li>The REGISTRAR of C-DOT who acts as ex-officio Secretary to the Project Board, the Steering Committee and the Governing Council is also responsible for various functions of the Centre including Personnel, Administration and Employee Services related to the working of professionals, health and welfare services, maintenance of the premises and the properties of the Centre, legal &amp; contractual, RTI matters, implementation of Official Language(HINDI). He shall be directly reporting to Executive Director, C-DOT who is the chairman of C-DOT Board.</li> </ul> <p><b>QUALIFICATIONS AND EXPERIENCE</b></p> <ul style="list-style-type: none"> <li>The applicant should be a Graduate in any discipline, preferable with Two Years, full time Professional Post Graduate qualification in Management from a reputed institute (with First Division) and at least 15 years of post-qualification professional experience in the areas mentioned above. Experience at</li> </ul>	Minimum 15 years

		<p>middle / senior level administrative posts in reputed Government/PSU, R&amp;D, Academic Institutes, other institutes, private sector will be preferred.</p> <ul style="list-style-type: none"> <li>• The applicant should be highly competent, people-focused motivating individual with Experience in the tasks and duties listed in the paragraph dealing with the “Profile of the Registrar”.</li> <li>• Applicants employed in the Central/ State Government Departments/ Undertakings/ Autonomous Bodies should be presently drawing pay in Level 13 of 7CPC of ₹123100-215900 or analogous scale with 2 years of experience. These officers can apply on deputation or absorption basis.</li> </ul> <p><b>AGE</b></p> <p>Not exceeding 50 Years as on 01.01.2022 (for internal candidates not exceeding 53 years)</p> <p><b>PAY SCALE, PERQUISITES &amp; BENEFITS</b></p> <ul style="list-style-type: none"> <li>• The post carries salary at the Government of India, Level 13A of 7CPC of ₹131100-216600. Besides this, the position carries allowances like central DA, HRA and benefits like medical reimbursement, subsidy for purchase of house/ conveyance, official transport, reimbursement of membership of professional bodies, Contributory Provident Fund, LTC, telephone facility, Gratuity etc. as per C-DOT rules, as applicable from time to time.</li> </ul>	
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C-0102	Post of CFO	<p>The Chief Finance Officer is the head of the Finance function in C-DOT. The primary responsibility of the Chief Finance Officer will be relating to Finance and Accounts of the Centre and he is responsible for maintenance and upkeep of Accounts</p> <p><b>Profile of Chief Finance Officer</b></p> <ul style="list-style-type: none"> <li>• The Chief Finance Officer will direct all finance-related strategies, initiatives and activities for C-DOT.</li> <li>• The CFO is also responsible for managing the funds of the Centre and Provident Fund, Gratuity Fund etc..</li> <li>• The Chief Finance Officer prepares the Annual Budget, Supplementary Budget Estimates for the Executive Director for submission to the Board and is responsible for the banking operations of the Centre and for the maintenance of Annual Accounts etc. as per the directive of Comptroller and Auditor-General of India.</li> <li>• He shall be directly reporting to Executive Director, C-DOT who is the chairman of C-DOT Board.</li> <li>• Designs and coordinates a wide variety of accounting and statistical data and reports</li> <li>• Monitoring and review of finance policies, procedures and practices in line with legal requirement .</li> <li>• Ensuring the timely completion and submission of financial and statistical reporting for management and regulatory agencies</li> <li>• Informing and advising senior management on the organization's financial condition. Working with senior management in setting annual financial goals</li> </ul>	Minimum 15 years

		<p>and targets for organization.</p> <ul style="list-style-type: none"> <li>• Tracking, reporting and managing the overall financial performance.</li> <li>• Any other assignment as given by higher authorities from time to time</li> </ul> <p><b>QUALIFICATIONS AND EXPERIENCE</b></p> <ul style="list-style-type: none"> <li>• The incumbent should be a highly competent Finance Professional, possessing exceptional interpersonal and communication skills, for C-DOT's top management post of the Chief Finance Officer.</li> <li>• The applicant should have at least Two Year's, full time MBA in Finance or ICWA/CA from a reputed institute and at least 15 years of post-qualification, professional experience in the areas mentioned above.</li> <li>• Applicants employed in the Central/ State Government Departments/ Undertakings/ Autonomous Bodies should be presently drawing pay in Level 13 of 7CPC of ₹123100-215900 or analogous scale with 2 years of experience. These officers can apply on deputation or absorption basis.</li> </ul> <p><b>AGE</b> Not exceeding 50 Years as on 01.01.2022 (for internal candidates not exceeding 53 years)</p> <p><b>PAY SCALE PERQUISITES &amp; BENEFITS</b> The post carries salary at the Government of India, Level 13A of 7CPC of ₹131100-216600. Besides this, the position carries allowances like central DA, HRA and benefits like medical reimbursement, subsidy for purchase of house/ conveyance, official transport, reimbursement of membership of professional bodies, Contributory Provident Fund, LTC, telephone facility, Gratuity etc.</p>	
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		as per C-DOT rules, as applicable from time to time.	
C-0103	Post of Sr. Manager/ Manager (Corporate Communications)	<p>The incumbent is responsible for coordinating the communications output within C-DOT. Creates and delivers emails, direct mail, brochures, newsletters, interfacing with external agencies and other publications. Good communication, presentation, writing, organizing, creative skills, as required to draft several documents, follow-up mails etc. Good grasping and convincing power. Good knowledge and understanding of C-DOT products and telecom technologies in general. Self-motivated and outgoing. Ability to organize marketing events, product demonstrations and promotional shows. Basic Knowledge of MS tools like Excel, Power point etc</p> <p><b>Profile of Sr. Manager/Manager (Corporate Communications)</b></p> <ul style="list-style-type: none"> <li>• Create C-DOT literature and other forms of communication.</li> <li>• Create marketing and promotional materials, both print and electronic.</li> <li>• Copyedit, proofread, and revise communications.</li> <li>• Design and launch email marketing campaigns.</li> <li>• Recommend, implement and maintain site design and operation.</li> <li>• Work with business units to determine event budget and manage expenses to that budget.</li> <li>• Promote products and services through public relations initiatives, social media, Press release etc.</li> <li>• Develop marketing communications campaigns.</li> <li>• Create thought leadership materials.</li> <li>• Create and deliver press releases, media relations content, case</li> </ul>	Minimum 10 years

		<p>studies, white papers, executive bios, corporate newsletter content, social media content, and speaking proposals.</p> <ul style="list-style-type: none"> <li>• Identify, develop and execute communications strategy for key media contacts and customer references.</li> <li>• Research media coverage and industry trends.</li> <li>• Develop fresh story ideas.</li> <li>• Conduct extensive media outreach manage social media campaign</li> <li>• Prepare briefing materials, Manage Pre-Sales activity with Marketing Team.</li> <li>• Coordinate scheduling and logistics.</li> <li>• Coordinate conference, trade shows, and press interviews.</li> <li>• Manage information.</li> <li>• Prepare agendas and conduct research.</li> <li>• To interact with product TLs/GLs and also with customers in regular meeting/follow-ups/negotiations.</li> </ul> <p><b>QUALIFICATIONS AND EXPERIENCE</b></p> <ul style="list-style-type: none"> <li>• The applicant should be a Graduate in any discipline and MBA in any discipline from a reputed institution. Experience of minimum 10 years in a reputed Advertising Agency, Sales function, Print, Radio, Electronic Media, Social Media.</li> </ul> <p><b>AGE</b> Not exceeding 45 Years as on 01.01.2022 (for internal candidates not exceeding 48 years)</p>	

Work Area Code	Work Area	SKILL SET	Experience
C-0104	Post of Sr. Manager/ Manager ( Start Up Operations)	<p>The Post of Sr. Manager/ Manager (Start Up Operations) requires that the candidate should have worked with start up companies (preferable). Should have taken the start up's to the next level. Should be capable of setting up the business from scratch.</p> <p><b>Profile of Sr. Manager/Manager (Start Up Operations)</b></p> <ul style="list-style-type: none"> <li>• Identify suitable startups for aligning with C-DOT'S R&amp;D work</li> <li>• Start incubation center in C-DOT</li> <li>• Onboarding of startups</li> <li>• Arrange funding for startups from C-DOT/External sources</li> <li>• Design workflows for contest launch, solution submission and automated model scoring and leader board</li> <li>• Handhold Startups to scale up their idea, innovation</li> <li>• Prepare a strategic plan for start up center and individual startups</li> <li>• Prepare market strategy for startups</li> <li>• Design and test business storyline</li> </ul> <p><b>QUALIFICATIONS AND EXPERIENCE</b></p> <p>Graduate from premier institute (IITs/IIMs/NIT) .Preferably MBA from reputed institute. Should have worked with start up eco system for a Minimum of 2 years.</p>	Minimum 2 years

		<p><b>Age</b> Not exceeding 45 Years as on 01.01.2022 (for internal candidates not exceeding 48 years)</p>	
C-0105	<p><b>Post of Sr. Manager/ Manager (Human Resource Development)</b></p>	<p>The post of Sr. Manager/Manager (Human Resource Development) requires a candidate to be well versed with HR policies and practices. Knowledge of rules relating to Performance Management System. Knowledge of DOPT rules, Promotion rules, government norms on reservation policy. Good drafting, presentation, interpersonal communication &amp; managerial skills. Good liaison experience with campuses for recruitment, HR consulting companies and manpower outsourcing vendors, etc. Good working knowledge of ERP practices.</p> <p><b>Profile of Sr. Manager/Manager (Human Resource Development)</b></p> <ul style="list-style-type: none"> <li>• Research, preparation &amp; proposing amendments in the existing policies for organization improvement and its employees.</li> <li>• Ensure smooth functioning of existing Performance appraisal system in-line with Organization's goals &amp; objectives through ERP.</li> <li>• Co-ordinate regular Grade Promotions as per existing policy.</li> <li>• Coordinate and conduct Responsibility Promotions as per organization requirement.</li> <li>• Campus recruitment and induction of manpower.</li> <li>• Co-ordinate and induct contractual manpower through outsourcing agencies.</li> <li>• Handling Grievance/disciplinary related matters and redressal.</li> <li>• Regular, routine activities related to HR functions – employee satisfaction survey, etc.</li> <li>• ERP implementation of HR functions &amp; practices.</li> <li>• Co-ordination &amp; implementation of</li> </ul>	<p>Minimum 10 years</p>



		<p>people CMMI/ RTIs &amp; Audits.</p> <p><b>QUALIFICATIONS AND EXPERIENCE</b></p> <p>The applicant should be an MBA(HR/OB) from a reputed institution. Minimum experience of 10 years in HR.</p> <p><b>Age</b>  Not exceeding 45 Years as 01.01.2022  (for internal candidates not exceeding 48 years)</p>	

**PROJECT – Radio Access Network (Project Code-T-01) & 5G RAN  
(Project Code-T-02) Positions (approx.)-50**

WORK AREACODE	WORK AREA	SKILL SET	Experience/Qualifications
<b>T-01</b>	<b>Leadership Position Covering aspects of RAN</b>	<ul style="list-style-type: none"> <li>• Knowledge of Wireless Technologies, Computer Networking, Data Networking, Signal Processing</li> <li>• Wireless network system architecture and subsystem design</li> <li>• 2G/3G/4G/5G Protocol understanding</li> <li>• Requirements development/management Process,</li> <li>• System Engineering Aspects,</li> <li>• Testing and debugging tools, Signal generator/analyzers, Logic Analyzers</li> <li>• Configuration Management, Scripting, Build and Development environment</li> <li>• Able to work on Product integration and testing like functional testing, load testing, Stability testing, Regression testing etc</li> <li>• Comfortable with organization processes</li> <li>• Interface with other teams and other organizations</li> <li>• Support for Field Trials and Testing</li> <li>• Lab building, maintenance</li> </ul> <p>C/C++ programming</p>	<p><b>Essential-</b> Minimum 5 years experience for B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>

<p><b>T-0101</b></p>	<p><b>Baseband HW</b></p>	<ul style="list-style-type: none"> <li>• Computer Architecture, Digital Logic, Processor/FPGA/SoC interfacing</li> <li>• Expertise in hardware design (Circuit Design and Analysis, PCB, Devices, Signal and power integrity, interface with CAD/VDG/EPG)</li> <li>• Testing and debugging tools, Signal generator/analyzers, Logic Analyzers</li> <li>• Knowledge of industry standards, standardization</li> <li>• Good understanding of 3GPP wireless standards</li> </ul>	
<p><b>T-0102</b></p>	<p><b>RRH Analog &amp; RF Hardware</b></p>	<ul style="list-style-type: none"> <li>• Computer Architecture, Digital Logic, Processor/FPGA/SoC interfacing</li> <li>• Expertise in hardware design (Circuit Design and Analysis, PCB, Devices, Signal and power integrity, interface with CAD/VDG/EPG)</li> <li>• Ability to learn and use RF analysis and design tools</li> <li>• Testing and debugging tools, Signal generator/analyzers, Logic Analyzers</li> <li>• Knowledge of industry standards, standardization</li> <li>• Good understanding of 3GPP wireless standards</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>
<p><b>T-0103</b></p>	<p><b>RRH Signal Processing in FPGA</b></p>	<ul style="list-style-type: none"> <li>• Computer Architecture, Digital Logic, Processor/FPGA/SoC interfacing</li> </ul>	

		<ul style="list-style-type: none"> <li>• Knowledge in hardware design (Circuit Design and Analysis, PCB, Devices, Signal and power integrity, interface with CAD/VDG/EPG)</li> <li>• Expertise in FPGA Development – Tools for FPGA development, Timing analysis, Languages (VHDL/Verilog), Simulation, Modeling, Programming</li> <li>• Matlab and Simulink and equivalent tools</li> <li>• Ability to learn and use RF analysis and design tools</li> <li>• Testing and debugging tools, Signal generator/analyzers, Logic Analyzers</li> <li>• SDR concepts, Waveforms, Digital Filters, Interfacing with IP from other vendors.</li> <li>• Knowledge of industry standards, standardization</li> <li>• Good understanding of 3GPP wireless standards</li> </ul>	
<p style="text-align: center;"><b>T-0104</b></p>	<p style="text-align: center;"><b>OS and Board Support Package</b></p>	<ul style="list-style-type: none"> <li>• System Engineering Aspects,</li> <li>• Testing and debugging tools, Signal generator/analyzers, Logic Analyzers</li> <li>• Configuration Management, Scripting, Build and Development environment</li> <li>• Able to work with Product integration and testing like functional testing, load testing, Stability testing, Regression testing etc</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>

		<ul style="list-style-type: none"> <li>• Processors, Devices, SoCs, FPGA, on-board devices and interface devices.</li> <li>• Operating Systems, distributions, packaging, devices, kernels, device drivers, data plane development, uboot, compilation, linking, loading</li> <li>• C/C++ programming skills along with scripting.</li> <li>• Power-on self test.</li> </ul>	
<p style="text-align: center;"><b>T-0105</b></p>	<p style="text-align: center;"><b>Physical Layer/Layer-1 Development</b></p>	<ul style="list-style-type: none"> <li>• Knowledge of LTE / UMTS / WCDMA / HSDPA/ 5G or WLAN standards in the physical layer and future developments within 3GPP, OFDM, MIMO</li> <li>• Testing and debugging tools</li> <li>• Experience with handling of specific test equipment (signal generator, signal analyser, logic analyser and pattern generator) is beneficial</li> <li>• Expertise in FPGA Development – Tools for FPGA development, Timing analysis, Languages (VHDL/Verilog), Simulation, Modeling, Programming</li> <li>• Matlab and Simulink and equivalent tools</li> <li>• DSP Programming skillset preferred</li> <li>• C/C++ programming skills along with scripting.</li> <li>• Unix Network Programming</li> <li>• Data Structures and</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/ M.Tech(CS/EC)/M.E.(CS/EC) /PhD (in relevant areas)</p>

		<p>Algorithms</p> <ul style="list-style-type: none"> <li>• Good understanding of 3GPP wireless standards</li> </ul>	
<b>T-0106</b>	<b>LTE Layer-2/Layer-3/RRM</b>	<ul style="list-style-type: none"> <li>• Testing and debugging tools</li> <li>• Configuration Management, Scripting, Build and Development environment</li> <li>• Able to work with Product integration and testing like functional testing, load testing, Stability testing, Regression testing etc</li> <li>• C/C++ programming skills along with scripting.</li> <li>• Unix Network Programming</li> <li>• Data Structures and Algorithms</li> <li>• Design Patterns</li> <li>• Familiarity with Protocol Stack Development</li> <li>• Good understanding of 3GPP wireless standards</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>
<b>T-0107</b>	<b>System Management (OAM/FCAPS)</b>	<ul style="list-style-type: none"> <li>• Testing and debugging tools</li> <li>• Configuration Management, Scripting, Build and Development environment</li> <li>• Able to work with Product integration and testing like functional testing, load testing, Stability testing, Regression testing etc</li> <li>• C/C++ programming skills along with scripting.</li> <li>• Unix Network Programming</li> <li>• Data Structures and Algorithms</li> </ul>	

		<ul style="list-style-type: none"> <li>• Design Patterns</li> <li>• Familiarity with Protocol Stack Development</li> <li>• Embedded Database based systems (mySQL, PostgreSQL)</li> <li>• System level understanding of network element.</li> <li>• Good understanding of 3GPP wireless standards</li> </ul>	
<b>T-0108</b>	<b>Self Organising Network</b>	<ul style="list-style-type: none"> <li>• Testing and debugging tools</li> <li>• Configuration Management, Scripting, Build and Development environment</li> <li>• Able to work with Product integration and testing like functional testing, load testing, Stability testing, Regression testing etc</li> <li>• C/C++ programming skills along with scripting.</li> <li>• Unix Network Programming</li> <li>• Data Structures and Algorithms</li> <li>• Design Patterns</li> <li>• Familiarity with Protocol Stack Development</li> <li>• Good understanding of 3GPP wireless standards</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>
<b>T-0109</b>	<b>Element Management System (EMS)</b>	<ul style="list-style-type: none"> <li>• Testing and debugging tools</li> <li>• Configuration Management, Scripting, Build and Development environment</li> <li>• Able to work with Product integration and testing like functional testing, load testing, Stability testing, Regression testing etc</li> </ul>	

		<ul style="list-style-type: none"> <li>• C/C++ programming skills along with scripting.</li> <li>• Unix Network Programming</li> <li>• Data Structures and Algorithms</li> <li>• JAVA Language</li> <li>• Web Development ( JSP,JavaScript,AJAX, HTML,Application Server, Web Server</li> <li>• Database (Knowledge of Oracle or MySql orPostgres)</li> <li>• ORM Framework</li> <li>• Framework (Sping Boot, Structs, BOOTSTRAP)</li> </ul>	
<b>T-0110</b>	<b>Cabinet Power and Accessories</b>	<ul style="list-style-type: none"> <li>• Computer Architecture, Digital Logic, Processor/FPGA/SoC interfacing</li> <li>• Expertise in hardware design (Circuit Design and Analysis, PCB, Devices, Signal and power integrity, interface with CAD/VDG/EPG)</li> <li>• Testing and debugging tools, Signal generator/analyzers, Logic Analyzers</li> <li>• Knowledge of industry standards, standardization</li> <li>• Systems engineering, thermal engineering, batteries, power aspects.</li> <li>• Antennas</li> <li>• Good understanding of 3GPP wireless standards</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/ M.Tech(CS/EC)/M.E.(CS/EC) /PhD (in relevant areas)</p>
<b>T-02</b>	<b>Leadership Position Covering aspects of 5G-RAN</b>	<p>Experience in Wireless product development, system/sub-system design and architecting. Product Integration especially in the domain of 4G/5G RAN.</p> <ul style="list-style-type: none"> <li>• Good understanding of</li> </ul>	<p><b>Essential-</b> Minimum 5 years experience for</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/ M.Tech(CS/EC)/M.E.(CS/EC) /PhD (in relevant areas)</p>



		<p>3GPP standards for 4G/5G, Open RAN architecture, O-RAN alliance specifications and protocols.</p> <ul style="list-style-type: none"> <li>• Working knowledge of physical layer and higher layers of eNodeB/gNodeB including open RAN interfaces.</li> <li>• Experience in Wireless conformance testing and evaluation for eNodeB/gNodeB using industry standard test equipment.</li> <li>• Working Knowledge of design tools like Matlab/ADS/FPGA tool and knowledge of generic server hardware, Linux OS, Accelerator and Network Interface cards.</li> <li>• Working knowledge in System Engineering, Radio link calculations and RAN synchronization procedures.</li> <li>• Possess leadership skills to manage team of engineers and interface with other teams. Familiarity with project planning, monitoring and configuration management.</li> </ul>	
<b>T-0201</b>	<b>(5G-RAN Software Development and Testing)</b>	<ul style="list-style-type: none"> <li>• Proficient in the C/C++ language, Data Structures, Algorithms, Linux, multi-threaded system and scripting</li> <li>• Good understanding of 3GPP, O-RAN and SCF specifications of 4G / 5G and RAN architecture,</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>

		<p>protocols, and procedures</p> <ul style="list-style-type: none"> <li>• Experience in software development, system design and architecting of protocol stacks and interfaces.</li> <li>• In-depth knowledge of RAN software development for mobile networks, especially on 3GPP LTE and NR. Working experience of 4G/5G Layer-2/Layer-3 software design and development.</li> <li>• Experience in development of RAN Intelligent Controller (RIC) and RIC interfaces.</li> <li>• Hands-on experience in Software integration and debugging issues. Experience in debugging tools like Wireshark, gdb etc</li> <li>• Experience in Open stack, DevOps CI/CD, Cloud, Kubernetes, Pods, Dockers will be an add on.</li> <li>• Experience with working on 5G test tools such as UE Simulators, Core Network Simulators</li> <li>• Experience with working in cloud native environment, experience with test and automation tools.</li> <li>• Shall be able to develop, enhance</li> </ul>	
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		<p>software protocols, interfaces and management software modules.</p> <ul style="list-style-type: none"> <li>• Good understanding of IP networking</li> <li>• Analyze system logs, test tool logs and use this information for debugging issues and identifying accurate root causes for the problems seen on the system under test or in the network.</li> </ul>	
<b>T-0202</b>	<b>(5G-RAN RF Unit development and Testing )</b>	<ul style="list-style-type: none"> <li>• Good understanding of 3GPP standards and O-RAN specifications of 4G / 5G and RAN architecture, protocols and procedures.</li> <li>• Working Knowledge of design tools like ADS/AWR essential.</li> <li>• Experience in Wireless conformance testing using industry standard test equipment will have added advantage.</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/ M.Tech(CS/EC)/M.E.(CS/EC) /PhD (in relevant areas)</p>
<b>T-0203</b>	<b>(5G-RAN Baseband-DU Physical Layer)</b>	<ul style="list-style-type: none"> <li>• Good understanding of 3GPP standards and O-RAN specifications of 4G / 5G and RAN architecture, protocols and procedures.</li> <li>• Experience in 4G/5G physical layer development and testing</li> <li>• Working knowledge of FAPI and nFAPI</li> </ul>	

		<p>for L1 &amp; L2 interface, and working knowledge of Openhaul 7-2x interface.</p> <ul style="list-style-type: none"> <li>• Proficient in C and C++ Knowledge and MATLAB</li> <li>• Experience in 4G/5G eNB/gNB testing with UE simulators, VSG and VSA</li> </ul>	
<b>T-0204</b>	<b>(5G-RAN Baseband-DU HW)</b>	<ul style="list-style-type: none"> <li>• Good understanding of 3GPP standards and O-RAN specifications of 4G / 5G and RAN architecture, protocols and procedures.</li> <li>• Experience in 4G/5G eNB/gNB testing with UE simulators, VSG and VSA.</li> <li>• Experience in programming in VHDL/Verilog for FPGA based designs and FPGA SOCs.</li> <li>• Working Knowledge in high speed interfaces like PCIe &amp; eCPRI and Knowledge of TCP/UDP.</li> <li>• Hands on experience for porting and running L1 on generic server. Experience in Linux OS, Real time kernels, DPDK and multi core programming in generic server</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>

		<p>hardware.</p> <ul style="list-style-type: none"> <li>• Working Experience in PCIe accelerator cards and Network interface cards(NIC).</li> <li>• Experience in implementing IEEE 1588 PTP on server</li> </ul>	
<b>T-0205</b>	<b>(5G-RAN Openhaul interface and Lower Physical layer development and Testing)</b>	<ul style="list-style-type: none"> <li>• Good understanding of 3GPP standards and O-RAN specifications of 4G / 5G and RAN architecture, protocols and procedures.</li> <li>• Knowledge of O-RAN front-haul interface specification (ORAN-WG4)</li> <li>• Experience in 4G/5G physical layer development and testing</li> <li>• Working Knowledge of simulation tools such as MATLAB and Digital signal Processing.</li> <li>• Working experience of OFDMA, MIMO, Beamforming, FFT/IFFT, Channel Estimation, channel equalizer and Carrier Aggregation implementation</li> <li>• Knowledge of NETCONF, SSH, TCP/IP, eCPRI, PTP and SyncE protocols</li> <li>• Working experience of FPGA based design and development</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>

## PROJECT – ITU COMMON ALERTING PROTOCOL (Project Code-T-03)

Positions (approx.) - 20

WORK AREA CODE	WORK AREA	SKILL SET	Experience/ Qualifications
T-03	<p><b>Leadership Position Covering aspects of ITU-Common Alerting Protocol</b></p>	<ul style="list-style-type: none"> <li>● Experience in building scalable, distributed, complex enterprise software systems that have been successfully delivered to customers.</li> <li>● Experience in contributing to the architecture and design (architecture, design patterns, reliability and scaling) of new and current systems</li> <li>● Experience in driving large projects from concept to prototype to production.</li> <li>● Experience working with databases, data processing, and perceptive tools.</li> <li>● Good understanding of common security threats and vulnerabilities and expertise to resolve and design secure software systems.</li> <li>● Knowledge of Big data Applications such as Kafka, MongoDB, Hadoop etc. is preferred.</li> <li>● Exposure to Machine Learning/Deep Learning projects and Knowledge of commonly used ML packages and libraries with MLOps concepts</li> <li>● Experience in administration and troubleshooting for DevOps/Deployment, Orchestration</li> </ul>	<p><b>Essential-</b> Minimum 5 years experience for B.Tech(CS/EC)/ B.E (CS/EC)/  M.Tech(CS/EC)/ M.E.(CS/EC) /PhD (in relevant areas)</p>

T-0301	<b>Software Solution Architecting and Development</b>	<ul style="list-style-type: none"> <li>● Experience in software design, development including delivering highly available services, excellent problem solving and system design skills</li> <li>● Experienced in design, problem solving, experience with <i>MVC</i> and coding skills with a strong bias for quality and object-oriented languages like- <i>JAVA</i> and <i>Python</i></li> <li>● Should have experience in contributing to the architecture and design (architecture, design patterns, reliability and scaling) of new and current systems</li> <li>● Work through all phases of the project lifecycle, including reviewing requirements, designing QA and support services for the new technology platform, building new test tools, developing test code</li> <li>● Experienced in Agile or Scrum software development methodologies, microservices and influencing software engineering best practices within the team</li> <li>● Driving the design and delivery of selected product features including collaborating with stakeholders on architecture, design/user experience, product structure and pricing strategy, and supportability</li> <li>● Proficiency in <i>HTML</i>, <i>Javascript</i> (frameworks like- <i>Angular</i>, <i>React</i>, <i>Vue</i>), <i>CSS</i> (libraries like- <i>Bootstrap</i>, <i>Foundation</i>, <i>Materialize</i>)</li> <li>● Good knowledge of distributed technologies for mobile</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/ B.E (CS/EC)/</p> <p>M.Tech(CS/EC)/ M.E.(CS/EC) /PhD (in relevant areas)</p>
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		<p>applications with real-time systems of high throughput, low latency, and highly scalable</p> <ul style="list-style-type: none"> <li>• Deep knowledge of distributed SOA architecture and design patterns</li> <li>• Engaging with customers to understand their requirements, pain points, and develop features and solutions to address their needs</li> <li>• Coding experience with automated testing frameworks, tools, and services</li> </ul>	
T-0302	<b>Cloud Technologies and Micro service Implementation</b>	<ul style="list-style-type: none"> <li>• Experience in administration and troubleshooting for DevOps/Deployment, Orchestration Management, and Configuration Management</li> <li>• Experience managing full application stacks from the OS up through custom applications</li> <li>• Deploying application into cloud using CICD(Continuous Integration and Continuous Deployment) process using docker, jenkins and kubernetes</li> <li>• Writing automation scripts to automate cloud deployment</li> <li>• Professional experience in architecting/deploying/operating applications on cloud</li> <li>• Experience in migration of conventional applications to the cloud</li> <li>• Advanced experience with System Administration with Linux (Ubuntu, CentOS, RedHat)</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/ B.E (CS/EC)/</p> <p>M.Tech(CS/EC) /M.E.(CS/EC) /PhD (in relevant areas)</p>
T- 0303	<b>Software Security and Network</b>	<ul style="list-style-type: none"> <li>• Experience in web application security, secure application</li> </ul>	<p>Minimum 2 years in Research and</p>



	<p><b>Security</b></p>	<p>design and architecture, threat modeling, secure coding, and cryptography</p> <ul style="list-style-type: none"> <li>● Deep technical understanding of OWASP Top 10 and CWE Top 25 vulnerabilities</li> <li>● Evaluating the impact of current security trends, and advisories and continuously updating the knowledge and skill set with rapidly changing threat landscape</li> <li>● Good understanding of common security threats and vulnerabilities</li> <li>● Ability to work with developers to resolve the security issues</li> <li>● Experience in code reviews, vulnerability detection, and root cause analysis</li> <li>● Experience in exploiting bugs and bypassing security mitigations in Operating Systems</li> <li>● Knowledge for use of IP protocol packets debugging tools as Wireshark, tcpdump, ping, traceroute, nmap, hping etc.</li> <li>● Knowledge of IDS/IPS (Intrusion Detection System/Intrusion Prevention System) monitoring and experience using at least one major SIEM system</li> <li>● Need a combination of troubleshooting, technical and communication skills</li> <li>● Should have strong sense of ownership, urgency and drive</li> <li>● Certifications in the security domain like CEH are preferred</li> </ul>	<p>Development</p> <p>B.Tech(CS/EC)/ B.E (CS/EC)/</p> <p>M.Tech(CS/EC)/ M.E.(CS/EC) /PhD (in relevant areas)</p>
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T- 0304	<b>Deep Learning and Natural Language Processing</b>	<ul style="list-style-type: none"> <li>● Demonstrated proficiency in data extraction, analysis and scripting tools</li> <li>● Experienced knowledge in Python with training and deploying machine learning models, using open source tools and libraries, such as: SciKit, TensorFlow, Pytorch etc.</li> <li>● Knowledge and Experience in developing linguistic annotation projects like Text to Speech and language conversion suitable for Indian Vernacular languages</li> <li>● Should have ability to analyze large datasets with text and image data, perform data visualizations, develop hypotheses, and make insightful recommendations</li> <li>● Exposure to Machine Learning/Deep Learning projects and Knowledge of commonly used ML packages and libraries with MLOps concepts</li> <li>● Knowledge of Big data Applications such as Kafka, MongoDB, Hadoop etc. is preferred.</li> <li>● Should have good communication and interpersonal skills</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/ B.E (CS/EC)/</p> <p>M.Tech(CS/EC)/ M.E.(CS/EC) /PhD (in relevant areas)</p>
T-0305	<b>Geospatial Solution Development and Image Processing</b>	<ul style="list-style-type: none"> <li>● Experience in developing GIS mapping applications and using geoprocessing tools</li> <li>● Experience using and working with imagery, mapping, and geospatial data and GIS location based software applications like tracking, route identification etc.</li> <li>● Advanced level knowledge of Geoserver and GIS Data</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC) /B.E (CS/EC)/</p> <p>M.Tech(CS/EC)/ M.E.(CS/EC) /PhD (in relevant areas)</p>

		<p>Management Tools like QGIS, MapInfo, ESRI ArcGIS and expertise in Openlayers5</p> <ul style="list-style-type: none"> <li>● Experience on implementing various Map and directional APIs with expertise in open source GIS map server and GIS software development</li> <li>● Expert level knowledge of Geodatabases and experience in design, and development of geospatial data models and geo-intelligent algorithms</li> <li>● Should have good communication and interpersonal skills</li> </ul>	areas)
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## PROJECT- Network Security (Project Code-04)

Positions (approx.) - 10

WORK AREA CODE	Work Area	SKILL SET	Experience/Qualifications
T-0401	<p style="text-align: center;"><b>Leadership Position in Field deployment, O&amp;M support</b></p>	<ul style="list-style-type: none"> <li>• Preferably person should have cybersecurity-related certification (e.g. Ethical hacking, Network forensic, Computer forensic etc.) course</li> <li>• Knowledge of shell scripting, Python scripting, Linux (CentOS/Ubuntu), Windows OS</li> <li>• Knowledge of TCP/IP Protocol, Routing Protocol as BGP, IPFIX protocol, DNS, DHCP, HTTP, HTTPs, SIP etc.</li> <li>• Knowledge for use of IP protocol packets debugging tools as Wireshark, tcpdump, ping, traceroute, nmap, hping etc.</li> <li>• Preferably knowledge of ICT device configuration like Router, Switches, PC, Servers etc.</li> <li>• Write effective documentation using MS Office &amp; Open Office tools for Doc, XLS and PPT etc.</li> </ul> <p>Knowledge of Big data Applications as Kafka, Spark, ELK stack, MongoDB, Hadoop etc.</p>	<p><b>Essential-</b> Minimum 5 years experience for B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>
T-0402	<p style="text-align: center;"><b>FPGA Programming</b></p>	<ul style="list-style-type: none"> <li>• Experience in FPGA software technology, from low-level hardware description languages like Verilog to high-level programming options like C, C++, and open computing language (OpenCL)</li> <li>• Knowledge of Xilinx FPGA cards, and tools like Vitis and Vivado for application development on FPGA</li> <li>• Good knowledge of Linux operating systems</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>

		<ul style="list-style-type: none"><li>• Coding knowledge of C, C++, shell scripting, Python scripting</li><li>• Knowledge of TCP/IP Protocol, Routing Protocol as BGP, IPFIX protocol, DNS, DHCP, HTTP, HTTPS, SIP etc.</li><li>• Knowledge of IP packet debugging tools such as Wireshark, tcpdump, ping, traceroute, nmap, hping etc.</li><li>• Good knowledge of packet capture, packet stripping, packet de-duplication, compression &amp; decompression, encryption &amp; decryption etc. to be implemented in FPGA</li></ul> <p>Selected candidates will be responsible to work on FPGA hardware and development tools to accelerate various functions of an application and to offload the CPU</p>	
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## PROJECT- 4G/5G CORE (Project Code-T05)

Positions (approx.)- 50

WORK AREA CODE	Work Area	SKILL SET	Experience/Qualifications
T-05	<b>Leadership Positions (Domain Experts) Covering aspects of 4G/5G Core and AI/ML/Analytics</b>	<ul style="list-style-type: none"> <li>• Shall have experience in development of 4G/5G network functions and CI/CD,</li> <li>• Shall have experience in algorithms, data sets and neural networks and concurrent/parallel programming</li> <li>• Shall have experience Design and development of communication networks analytics</li> <li>• Shall have experience in architecting products, leading products development and delivery and in managing large teams</li> </ul>	<b>Essential-</b> Minimum 5 years experience for B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)
0501	<b>4G/5G Technology Development</b>	<ul style="list-style-type: none"> <li>• Shall have experience on Mobile Technologies (4G/5G)</li> <li>• Shall have experience of protocol software development</li> <li>• Shall have experience of large database-oriented application development</li> <li>• Shall have good expertise in development of Internet Protocol based applications</li> <li>• Shall have good knowledge of networking concepts</li> </ul>	Minimum 2 years in Research and Development  B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)

		<ul style="list-style-type: none"><li>• Should have experience in developing software utilizing various coding languages including C/C++, goLang, rust, java, web technologies etc.</li><li>• Should have experience in analyzing code for weaknesses and errors, and on how to improve them.</li><li>• Should have experience in Data Analytics Software</li><li>• Should have experience in developing customized code for multiple projects simultaneously</li><li>• Should have experience in developing secure software systems based upon industry specifications</li><li>• Should have experience in software design using large data sets</li><li>• Should have experience in using cybersecurity</li><li>• Should have experience in using open source technology</li><li>• Should have experience of Management Interface development</li><li>• Should have experience working on large-scale software projects</li><li>• Should have good knowledge of CI/CD</li><li>• Should have worked cloud based software development</li><li>• Should possess eagerness to explore</li></ul>	
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		<p>and learn new technologies</p> <ul style="list-style-type: none"> <li>• Should possess outstanding teamwork spirit, collaboration and communication skills</li> <li>• Preference would be given for candidates having knowledge of 4G/5G protocols</li> <li>• Preference would be given to candidates having containers / virtual machine-based application development experience.</li> </ul>	
<b>0502</b>	<b>AI/ML/Analytics</b>	<ul style="list-style-type: none"> <li>• Shall have expertise in Mathematical Software, Linear Programming, Linear Algebra and Optimization, AI, ML and Analytics</li> <li>• Shall have experience in developing mathematical software, parallel programming, multi-processing and high-performance computing</li> <li>• Should have experience of large database-oriented application development</li> <li>• Should have experience in developing software utilizing various coding languages including C/C++, goLang, rust, java, web technologies etc.</li> <li>• Should have experience in analyzing code for weaknesses and errors, and on how to improve</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>



		<p>them.</p> <ul style="list-style-type: none"><li>• Should have experience in Data Analytics Software</li><li>• Should have experience in developing customized code for multiple projects simultaneously</li><li>• Should have experience in software design using large data sets</li><li>• Should have experience in using open source technology</li><li>• Should have experience working on large-scale software projects</li><li>• Should have good knowledge of CI/CD</li><li>• Should possess eagerness to explore and learn new technologies</li><li>• Should possess outstanding teamwork spirit, collaboration and communication skills</li></ul>	
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## PROJECT- QUANTUM COMMUNICATION (Project Code-06)

### Positions (approx.)- 05

WORK AREA CODE	Work Area	SKILL SET	Experience/Qualifications
0601	Leadership Position Covering aspects of Quantum Mechanics	<ul style="list-style-type: none"><li>• Strong Theoretical Background in Quantum Communications</li><li>• Expertise in analysing QKD Protocols (&amp; security proofs for the same) and support teams implementing the same</li><li>• Practical QKD implementation experience desirable</li><li>• Good understanding of various types of attack strategies on different QKD protocol implementations</li><li>• Should have thorough knowledge of Quantum Mechanics in general and all</li></ul>	<b>Essential</b> - Minimum 5 years experience for B.Tech(CS/EC)/B.E (CS/EC)/ M.Tech(CS/EC)/M.E.(CS/EC) /PhD (in relevant areas)

		<p>aspects related to Quantum Communications in particular</p> <ul style="list-style-type: none"> <li>• Strong documentation and presentation skills; must be proficient in relevant software tools used in the same</li> </ul>	
<b>0602</b>	FPGA Programming	<ul style="list-style-type: none"> <li>• Experience in FPGA design, from low-level hardware description languages like Verilog/VHDL to high-level programming options like C, C++, and open computing language (OpenCL)</li> <li>• Knowledge of Xilinx FPGA cards, and tools like Vivado for application development on FPGA, IP Core development and integration</li> <li>• Experience of working on Optical Communication products desirable</li> <li>• Strong digital design concepts as well as knowledge of Linux operating system</li> <li>• Coding knowledge of C, C++, shell scripting, Python scripting</li> <li>• Knowledge of Ethernet &amp; other telecom protocols, error correction, privacy amplification, synchronization etc.</li> <li>• Experience in handling lab instruments for debugging e.g. Mixed Signal Oscilloscope, signal generators etc.</li> <li>• Experience in FPGA Timing closure</li> <li>• Experience working with SoC based systems and knowledge of protocols such as SPI, UART, I2C, USB.</li> <li>• Strong documentation and presentation skills; must be proficient in relevant software tools used in the same</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>

0603	Optical Communication	<ul style="list-style-type: none"> <li>• Strong optical communication fundamentals</li> <li>• Experience of designing optical communication products</li> <li>• Good theoretical and practical knowledge of optical devices used in Optical Communication and Quantum Communications</li> <li>• Good Board design skills and proficiency in tools used for the same</li> <li>• Strong analog design concepts and proficiency in tools used for the same</li> </ul> <p>Strong documentation and presentation skills; must be proficient in relevant software tools used in the same</p>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>
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**PROJECT- Post-Quantum Cryptography (ProjectCode-07)**  
**Positions (approx.) - 05**

WORK AREA CODE	Work Area	SKILL SET	Experience/Qualifications
0701	Embedded Software	<ul style="list-style-type: none"> <li>• Embedded Programming with different embedded MPU and MCUs , C and ARM assembly programming, GNU toolchain, Bootloader and Secure Boot development, Board bringup, Buildroot, RTOS and FreeRTOS based development</li> <li>• Experience in Baremetal software development, Linux kernel and Device Driver programming, Open Source Software development, TCP/IP stack development</li> <li>• Experience working with FPGA or SOC based systems, Yocto, Python</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>

		<p>scripting, GUI development using Qt/QML</p> <ul style="list-style-type: none"> <li>• Experience working with lab tools (Oscilloscope, Debuggers etc.), Linux and RTOS based system programming experience</li> </ul>	
0702	FPGA Design & Implementation	<ul style="list-style-type: none"> <li>• Good Verilog/VHDL programming skills, FPGA design development experience</li> <li>• Exp. with FPGA synthesis tool- Xilinx Vivado, Altera Quartus II or similar, IP Core development and integration</li> <li>• Experience with Mixed Signal Oscilloscope usage</li> <li>• Experience in FPGA Timing closure</li> <li>• Experience in TCL/Tk and Python scripting, Good C programming skills</li> <li>• Experience in Linux kernel and Device Driver Programming, Experience in Baremetal and FreeRTOS based system development</li> <li>• Experience working with SoC based systems</li> <li>• Lab experience probing boards and debugging. Protocol knowledge of peripherals: SPI, UART, I2C, USB.</li> </ul>	<p>Minimum 2 years in Research and Development  B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>
0703	Post-Quantum Cryptography Algorithms Implementation & Testing	<ul style="list-style-type: none"> <li>• Experience in Design, Implementation &amp; Testing of Post-Quantum Cryptography (PQC) Algorithms, Side-Channel Analysis &amp; Testing</li> <li>• Experience in working in Security Projects.</li> </ul>	<p>Minimum 2 years in Research and Development  B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>

0704	Post-Quantum Cryptography Algorithms Theoretical Analysis & Cryptanalysis	<ul style="list-style-type: none"> <li>• Strong Theoretical background in Cryptography. Experience of Theoretical Analysis on Lattice and Code based PQC algorithms, analysis of security strengths</li> <li>• Study of security attacks, cryptanalysis, Side-channel analysis etc.</li> </ul>	<p>Minimum 2 years in Research and Development</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>
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### PROJECT- Validation and Field Support (ProjectCode-08)

Positions (approx.) - 15

WORK AREA CODE	Work Area	SKILL SET	Experience/Qualifications
0801	Networking Domain	<ul style="list-style-type: none"> <li>• Working knowledge of TCP/IP protocol Suite, networking devices like Router , Switch(L2 &amp; L3) ,</li> <li>• VoIP, Firewall , Access Points etc.</li> <li>• Network Configuration and testing.</li> <li>• Understanding of IP/MPLS and Ethernet technologies , Switching and Routing protocols (STP, RSTP, MSTP, OSPF, BGP, RIP, EIGRP etc. ), SIP protocols etc.</li> </ul>	<p>Minimum 2 years in Research and Development / Field Support/Validation in relevant area</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)</p>

		<ul style="list-style-type: none"> <li>• Network Administration, Systems/network Integration, Troubleshooting, Operation &amp; Maintenance, understanding of networking security</li> <li>• CCNA/CCNP certification preferred.</li> </ul>	
<b>0802</b>	Field Support (Telecom & IP Network)	<ul style="list-style-type: none"> <li>• Candidate shall have experience of installation, deployment and support of different elements of 2G/3G/4G network</li> <li>• Candidate shall have experience of working on Network management and troubleshooting.</li> <li>• Hands-on experience of working on UNIX/Linux environment, shell scripting will be an additional advantage</li> <li>• Candidate shall have experience of working on Cloud environment.</li> <li>• Knowledge of Architecture of EPC &amp; IMS as well as different Call flows in 4G/Volte Network</li> <li>• Good grasp on different Debugging Tools like Wireshark, Tcpdump, Tethereal</li> <li>• Knowledge of VOIP, SIP, Megaco, Diameter, MAP, GTP etc.</li> <li>• Knowledge of Networking Protocols i.e. OSPF, BGP, ACL, MPLS etc.</li> <li>• Comfortable with frequent Travelling/Field Visit at Customer sites</li> <li>• Good verbal and oral communication skill</li> </ul>	<p>Minimum 2 years in Research and Development/ Field Support/Validation in relevant area</p> <p>B.Tech(CS/EC)/B.E (CS/EC)/ M.Tech(CS/EC)/M.E.(CS/EC) /PhD (in relevant areas)</p>

**PROJECT- Artificial Intelligence (ProjectCode-09)****Positions (approx.) - 5**

<b>WORK AREA CODE</b>	<b>Work Area</b>	<b>SKILL SET</b>	<b>Experience/Qualifications</b>
<b>0901</b>	<b>Leadership Position in AI backend pipeline design and development</b>	<ul style="list-style-type: none"><li>• Kafka and spark architectures</li><li>• Experience in AI backend pipeline design and development</li><li>• C/C++, Java, Python, R and Scala</li></ul>	<b>Essential-</b> Minimum 5 years experience for B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)
<b>0902</b>	<b>Leadership Position in Natural Language Processing</b>	<ul style="list-style-type: none"><li>• Should have sound knowledge of machine learning and deep learning concepts, NLP, should have acquaintance in various frameworks like tensorflow, keras, pytorch, Java, Python etc.</li></ul>	<b>Essential-</b> Minimum 5 years experience for B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)
<b>0903</b>	<b>Leadership Position in Security evaluations and test automations and load testing tools</b>	<ul style="list-style-type: none"><li>• Experience in handling security issues, protocols and mechanisms, security evaluations, server high-availability, load-balancing, proxy configurations; development of load testing tools, performance, health monitoring and service recovery scripts , docker/container configurations for server solutions is preferred.</li></ul>	<b>Essential-</b> Minimum 5 years experience for B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)
<b>0904</b>	<b>Computer Vision using Deeplearning</b>	<ul style="list-style-type: none"><li>• Deeplearning Architectures,</li><li>• C/C++,</li><li>• Python,</li><li>• Caffe,</li><li>• PyTorch, Keras and Tensorflow, OpenCV</li></ul>	Minimum 2 years in Research and Development  B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)
<b>0905</b>	<b>Big Data Search over Metadata generated by AI engine/ Augmented Reality/Visual Positioning</b>	<ul style="list-style-type: none"><li>• HADOOP, MongoDB &amp; Elastic Search</li><li>• Augmented Reality Application Development</li><li>• Visual Positioning application Development</li></ul>	Minimum 2 years in Research and Development  B.Tech(CS/EC)/B.E (CS/EC)/M.Tech(CS/EC)/M.E.(CS/EC)/PhD (in relevant areas)